

SYSTEMS AND METHODS FOR PARTITIONING BANKS OF PROCESSORS IN
LARGE COMPUTER SYSTEMS

ABSTRACT OF THE DISCLOSURE

Systems and methods for operatively connecting processor banks in large computer systems are disclosed herein. In one embodiment, a computer system includes a first bank of processors, a second bank of processors spaced apart from the first bank of processors, and a connector assembly configured to operatively connect at least a portion of the first bank of processors to at least a portion of the second bank of processors. The connector assembly can include a first connector unit having a plurality of first connector sets and a second connector unit having a plurality of corresponding second connector sets. At least one of the first and second connector units is movable relative to the other one of the first and second connector units to at least approximately concurrently engage the plurality of first connector sets with the plurality of corresponding second connector sets.